

FIG. 1

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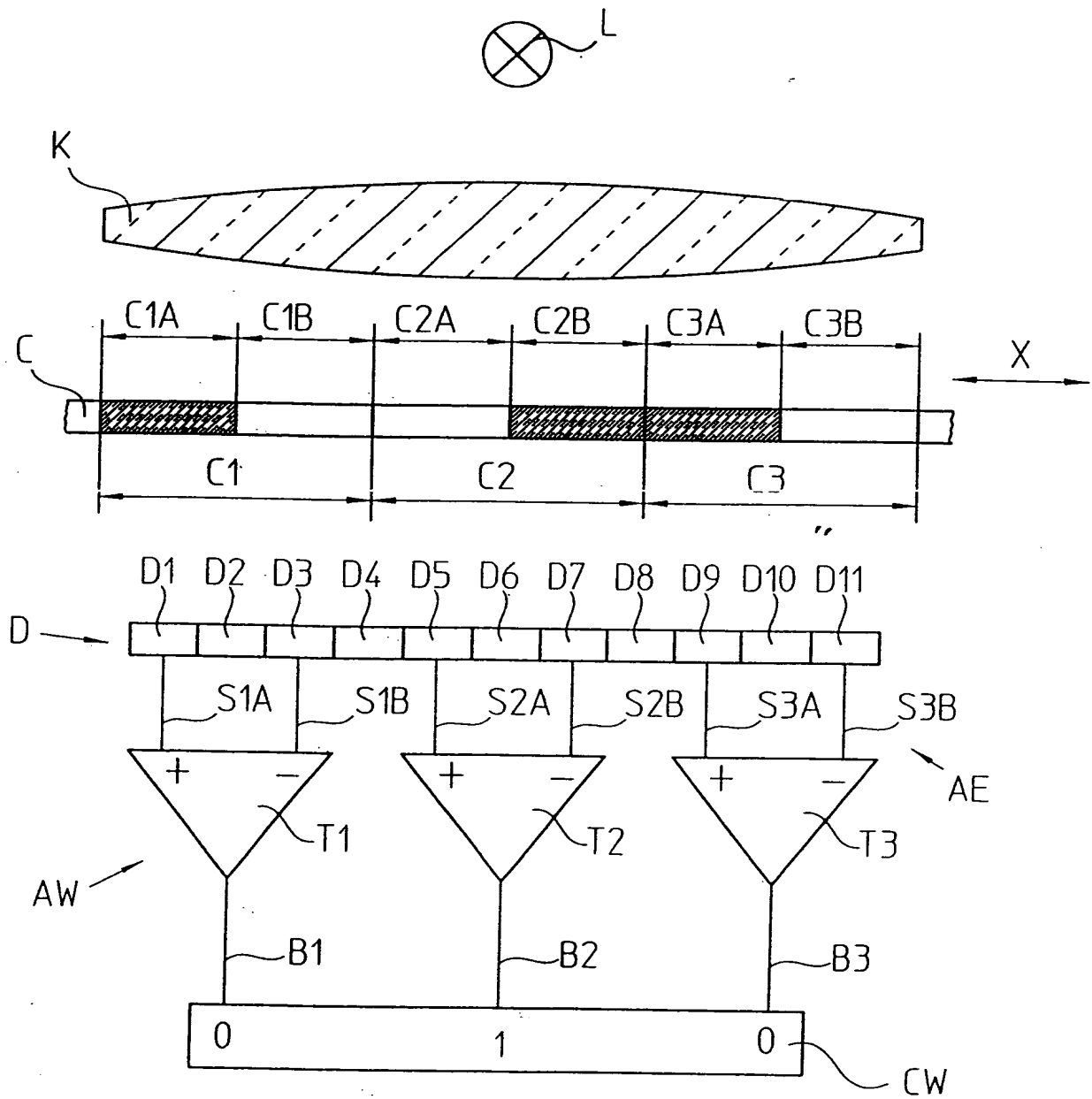


FIG. 2

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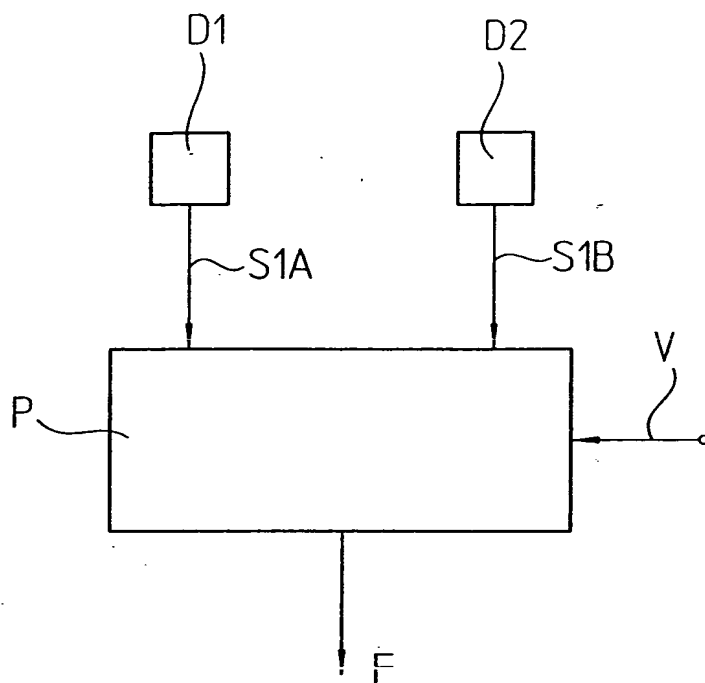


FIG. 3

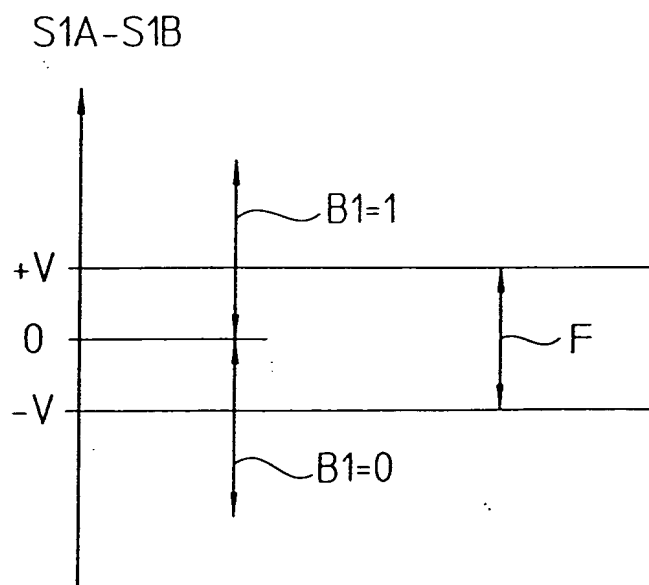


FIG. 4

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3-Bit PRC -> 1110100011101

Cyclical expansion

Table of Association

Position	Address
First Position:	111010
Second Position:	110100
Third Position:	101000
Fourth Position:	010001
Fifth Position:	100011
Sixth Position:	000111
Seventh Position:	001110
Eighth Position:	011101

FIG. 5a

Position:	111010	110100	101000	010001	100011	000111	001110	011101
Scanning:	110100	110100	110100	110100	110100	110100	110100	110100
Comparison:	xx__x	xxxxxx	x__xx	_xx_x_	x_x__	_xx__	_x_x_	_x_xx_

Result : One match for a predetermined second position -> Position valid

FIG. 5b

Position:	111010	110100	101000	010001	100011	000111	001110	011101
Scanning:	F10100	F10100	F10100	F10100	F10100	F10100	F10100	F10100
Comparison:	xx__x	xxxxxx	x__xx	xxx_x_	x_x__	x_xx__	x_x_x_x	xx_xx_

Result : One match for a predetermined second position -> Position valid

FIG. 5c

Position:	111010	110100	101000	010001	100011	000111	001110	011101
Scanning :	F10F00	F10F00	F10F00	F10F00	F10F00	F10F00	F10F00	F10F00
Comparison :	xx_x_x	xxxxxx	x__xxx	xxxxx_	x_xx__	x_xx__	x__x_x	xx_xx_

Result : One match for a predetermined . Second position : -> Position valid

FIG. 5d

Position:	111010	110100	101000	010001	100011	000111	001110	011101
Scanning :	F10FF0	F10FF0	F10FF0	F10FF0	F10FF0	F10FF0	F10FF0	F10FF0
Comparison :	xx_xxx	xxxxxx	x__xxx	xxxxx_	x_xxx_	x_xxx_	x__xxx	xx_xx_

Result : One match for a predetermined . Second position : -> Position valid

FIG. 5e

Position:	111010	110100	101000	010001	100011	000111	001110	011101
Scanning :	F10F0F	F10F0F	F10F0F	F10F0F	F10F0F	F10F0F	F10F0F	F10F0F
Comparison :	xx_x_x	xxxxxx	x__xxx	xxxxxx	x_xx_x	x_xx_x	x__x_x	xx_xxx

Result : Two matches for a predetermined second position and fourth position -> Scanning invalid, position cannot be determined

FIG. 5f

Position:	111010	110100	101000	010001	100011	000111	001110	011101
Scanning :	110000	110000	110000	110000	110000	110000	110000	110000
Comparison :	xx_x_x	xxx_xx	x__xxx	_xxxx_	x_xx__	_x__	_x__	_x_x_

Result : No matches -> Erroneous bits are included that were not marked